

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,385,268 B2  
APPLICATION NO. : 10/506654  
DATED : June 10, 2008  
INVENTOR(S) : Mark N. Horenstein

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Pg, insert after Prior Publication section Item (60):

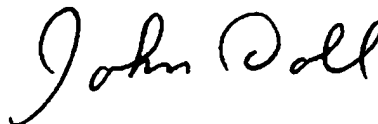
--Related U.S. Application Data

(60) Provisional Application No. 60/363,139, filed on March 8, 2002.--; and

Column 1, delete lines 17-19 and insert, --This application is a 371 of PCT/US03/07123 filed March 10, 2003 and claims priority of US Provisional Application No. 60/363,139, filed March 8, 2002, entitled: METHOD FOR LINEARIZING DEFLECTION OF A MEMS DEVICE USING BINARY ELECTRODES AND VOLTAGE MODULATION.--.

Signed and Sealed this

Ninth Day of June, 2009



JOHN DOLL

*Acting Director of the United States Patent and Trademark Office*